

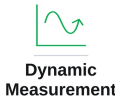
RUGGED SINGLE-CHANNEL STRAIN GAUGE

AMPLIFIER



Type: **KRYPTONi-1xSTG**

- 1 CHANNEL, 24 BIT
- 40 kS/s PER CHANNEL
- FULL, HALF, QUARTER BRIDGE
- IP65 / IP67 · -40 ... +85 °C



ORDER CODE

Order code	Type	Connectors
EMDC	KRYPTONi-1xSTG	1 x DSUB-9 female

DSI adapters, mating connectors and EtherCAT cables are accessories.



OVERVIEW

Data acquisition brought to the sensor instead of the sensor to the rack: one universal bridge channel in an aluminium block machined from solid, sealed and rated IP65 / IP67 from -40 to +85 °C. Each channel takes a full, half or quarter bridge — completion for 120 Ω and 350 Ω is built in, and so is the 100 kΩ shunt for the calibration check — or a voltage in the ranges ±50 V · ±10 V · ±1 V · ±100 mV, digitised with 24 bit at up to 40 kS/s per channel. A built-in 50 Ω shunt turns the same input into a current input for 20 mA and 2 mA. One hybrid cable carries data, power and synchronisation: EtherCAT at 100 Mbit/s, up to 100 m to the next unit, up to 64 units in a chain, all sampled to within less than one sample of each other. TEDS is read automatically, and DSI adapters extend the same channel to IEPE, thermocouple, RTD, LVDT and charge.

MEASUREMENT	SYSTEM AND ENVIRONMENT
• 1 universal channel: full, half or quarter bridge, or voltage • 24-bit SAR with anti-aliasing filter • Simultaneous 40 ks/s per channel, software-selectable • Ranges ±50 V · ±10 V · ±1 V · ±100 mV · ±0.03 % of reading • Bridge ranges 2 ... 1000 mV/V, freely programmable • Internal completion for 120 Ω and 350 Ω half and quarter bridges • 100 kΩ shunt on board for the calibration check • Built-in current input for 20 mA and 2 mA with a 50 Ω shunt	• EtherCAT at 100 Mbit/s — one cable for data, power and sync • Up to 100 m between units, daisy-chainable • 64 units or more in one chain (power injectors may be required) • Synchronised to within less than one sample • IP65 / IP67 · machined from solid aluminium, sealed and rubber-banded • -40 °C to +85 °C · humidity 5 to 100 % RH • Shock 100 g, 6 ms half-sine · random vibration to MIL-STD-202G • TEDS read automatically · DSI adapters for IEPE, TC, RTD, LVDT, charge

TECHNICAL DATA

ANALOG INPUT

Input type	voltage · full, half and quarter bridge strain · current
Number of channels	1
ADC type	24-bit SAR with anti-aliasing filter
Sampling rate	simultaneous 40 kS/s per channel, software-selectable
Voltage ranges	$\pm 50\text{ V}$ · $\pm 10\text{ V}$ · $\pm 1\text{ V}$ · $\pm 100\text{ mV}$
Input accuracy	$\pm 0.03\%$ of reading · $\pm 0.02\%$ of range · $\pm 0.1\text{ mV}$
Input coupling · impedance	DC, AC 1 Hz · 1 M Ω , 20 M Ω depending on the range
Overvoltage protection	200 V · 50 V depending on the range
Current ranges	20 mA · 2 mA
Current accuracy	$\pm 0.03\%$ of reading · $\pm 0.02\%$ of range · $\pm 2.1\text{ }\mu\text{A}$
Internal shunt for current	50 Ω

SIGNAL AND PRECISION

Gain drift	typ. 10 ppm/K, max 40 ppm/K
Offset drift	typ. 0.3 $\mu\text{V/K}$ + 5 ppm/K
Gain linearity	< 0.02 %
Bandwidth (-3 dB)	0.49 fs
Alias-free bandwidth	DC to 0.453 fs
Alias rejection	-100 dB, all sample rates
Oversampling	32
Delay through ADC	37 / fs

BRIDGE AND EXCITATION

Bridge connection	full · half · quarter bridge (3-wire)
Bridge ranges	2 ... 1000 mV/V, freely programmable
Internal completion	half and quarter bridge, 120 Ω and 350 Ω
Completion accuracy	0.05 % · TCR 2 ppm/K (others on request)
Internal shunt	100 k Ω (others on request) · 0.1 % · TCR 10 ppm/K
Excitation voltage	freely programmable, 16-bit DAC · preset unipolar 0 ... 24 V, bipolar 0 ... 12 V
Excitation accuracy · drift	$\pm 0.05\%$ $\pm 2\text{ mV}$ · $\pm 50\text{ ppm/K}$ ± 100 $\mu\text{V/K}$
Load stability 10 ... 90 %	< 0.01 %
Current limit · protection	42 mA, 400 mW max · continuous short to ground
Input short, offset adjust	software-selectable

INTERFACE AND POWER

Interface	EtherCAT · 100 Mbit/s full duplex
Connection	LEMO 1T hybrid cable — data, power and sync in one cable, daisy- chainable
Max. cable length	100 m between units
Max. units per chain	64 or more (power injectors may be required)
Throughput per chain	6 ... 10 MB/s, depending on the configuration
Sync accuracy	below one sample
Power supply	9 ... 48 V DC
Power consumption	2.8 W typical
Isolation	125 V rms channel to ground

PERFORMANCE PER VOLTAGE RANGE

Voltage range	$\pm 50\text{ V}$ · $\pm 10\text{ V}$ · $\pm 1\text{ V}$ · $\pm 100\text{ mV}$
Typ. SNR at 10 kS/s	89 · 88 · 88 · 87 dB
Typ. SFDR at 10 kS/s	100 · 99 · 100 · 96 dB
Typ. noise floor at 10 kS/s	-103 · -99 · -103 · -96 dB
Typ. CMR at 400 Hz / 1 kHz	71/66 · 72/66 · 96/88 · 96/87 dB



ENVIRONMENT AND MECHANICS

Operating temperature	-40 °C ... +85 °C
Storage temperature	-40 °C ... +85 °C
Humidity	5 ... 100 % RH
Protection class	IP65 / IP67 · depending on the mating connectors
Shock	100 g, 6 ms half-sine to SIST EN 60068-2-27:2009 · 100 x pos./neg. per axis, 600 x total
Vibration	random to MIL-STD-202G method 214A, condition II-D · 13.9 g RMS, 41.7 g peak · 8 h per axis, 24 h total
Housing	aluminium, machined from solid, sealed
Weight	250 g
Dimensions	62 x 56 x 29 mm

SENSORS AND CONNECTION

Input connector	1 x DSUB-9 female, one per channel
TEDS	read automatically · standard and through DSI adapters
IEPE · charge	with the DSI-ACC and DSI-CHG adapters
Thermocouple · RTD	with the DSI-TH and DSI-RTD adapters
LVDT	with the DSI-LVDT adapter
Potentiometer	directly
Current	built in, 20 mA and 2 mA
Software	DewesoftX included · licence embedded in the device



ACCESSORIES: DSI ADAPTERS, POWER AND CABLE

DSI-ACC	IEPE and ICP sensors · 4 mA / 21 V, high-pass 0.8 Hz · versions with 0.16 Hz, with 20 mA, and DSIw-ACC in an IP67 housing
DSI-CHG	charge · DSI-CHG-50 up to 50 000 pC, AC 0.07 Hz, 100 kHz · DSI-CHG-DC 1 ... 500 kpC, drift < 0.1 pC/s, expressly supported by the STG amplifiers
DSI-TH	thermocouples · DSI-TH-UNI for K, J, T, R, S, N, E, B and C with cold junction compensation to 0.5 °C · single types as DSI-TH-K, -J, -T, -E, -C
DSI-RTD	PT100, PT200, PT500, PT1000 and PT2000 · 2-, 3- and 4-wire · Binder 710 mating connector included
DSI-LVDT	LVDT sensors · excitation 4 ... 10 kHz, phase adjustment by trimmer · output 1 V = 1000 mV/V
Current adapters	DSI-20mA with a 50 Ω internal shunt, 0.01 % · DSI-5A with 10 m Ω , 0.1 % · DSli-10A and DSli-20A isolated, < 0.5 %
Every DSI adapter	TEDS chip to IEEE 1451.4, detected automatically · aluminium DSUB-9 housing with screw locking · IP20, the DSIw types IP67
Power supply	PS-120-L1T8m 24 V DC, 5 A, 120 W · PS-200-L1T8m 24 V DC, 8.3 A, 200 W · 8-pole LEMO male, connected to the OUT socket
EtherCAT cable	DS-EtherCAT, 8-pole LEMO L1T8m to L1T8f · data, power and sync between the modules

Some DSI adapters are restricted to certain host amplifiers — the manufacturer's accessories manual lists the exceptions.



COMPLIANCE AND CERTIFICATION



RoHS Directive 2011/65/EU — no intentional use of restricted or prohibited hazardous substances

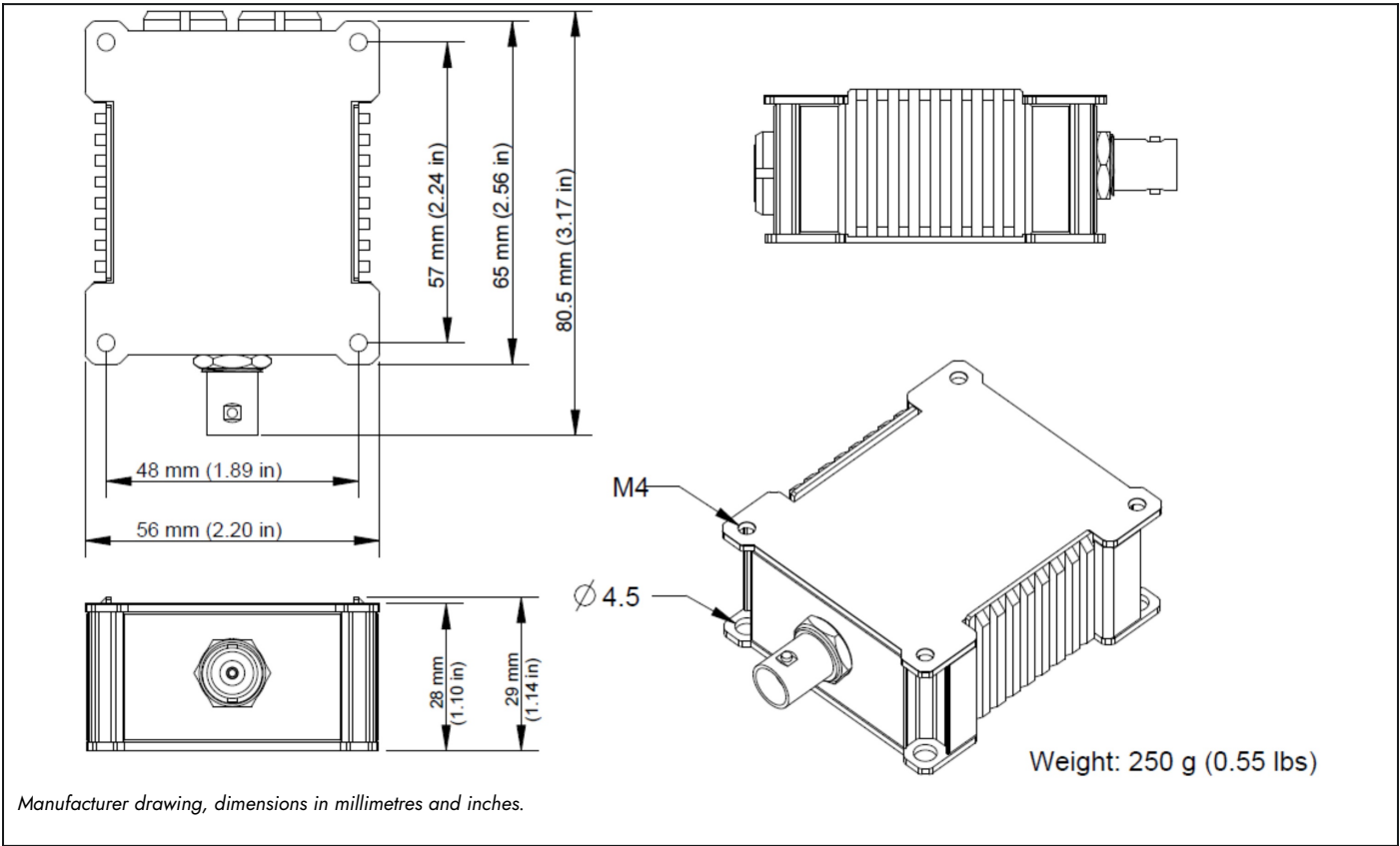


REACH Regulation (EC) No 1907/2006

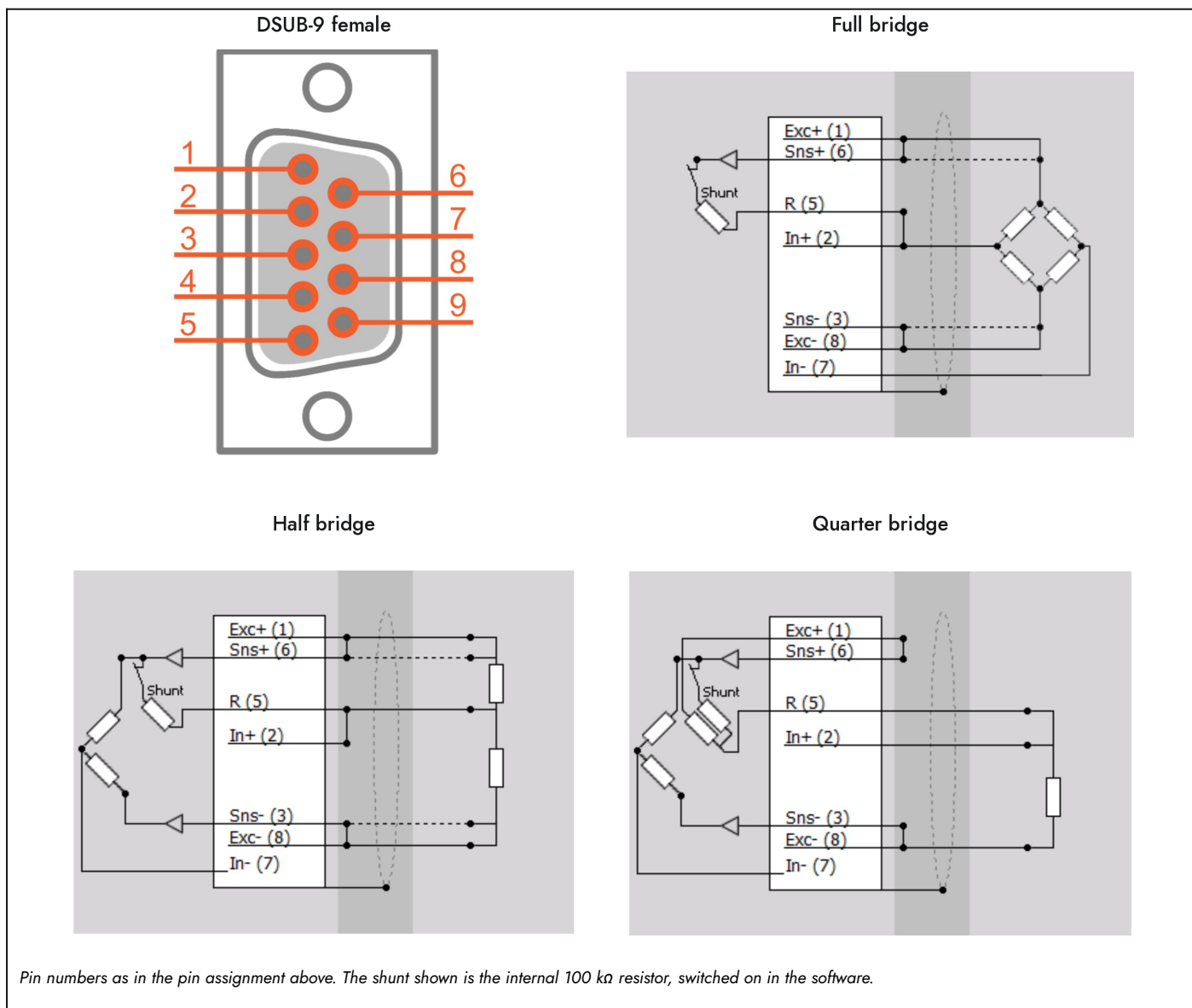
CALIBRATION AND DISPOSAL

Delivery	each device is calibrated before delivery
Reports	detailed calibration reports on request · retained for at least one year after delivery
Interval	annual calibration is the usual standard across industries
Disposal	WEEE Directive 2012/19/EU — collected separately at the end of service life, not as unsorted municipal waste · the housing is aluminium and selected components are suitable for recycling

DIMENSIONS (mm)



CONNECTOR AND BRIDGE CONNECTION



PIN ASSIGNMENT, DSUB-9 FEMALE

Pin	Name	Function
1	Exc+	Excitation +
2	In+	Input +
3	Sns-	Sense -
4	GND	Ground
5	R+	¼ bridge / shunt
6	Sns+	Sense +
7	In-	Input -
8	Exc-	Excitation -
9	TEDS	TEDS

